UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/749,034	12/30/2003	Miguel A. Guerrero	INTCP007	8288
Iung-Hua Kuo	7590 07/20/2007 Attorney at Law	EXAMINER		
Jung-Hua Kuo Attorney at Law c/o PortfolioIP P.O. Box 52050 Minneapolis, MN 55402			MOUTAOUAKIL, MOUNIR	
			ART UNIT	PAPER NUMBER
,			2616	
	. 🔻		ı 🗗	
			MAIL DATE	DELIVERY MODE
			07/20/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		TH			
	Application No.	Applicant(s)			
	10/749,034	GUERRERO ET AL.			
Office Action Summary	Examiner	Art Unit			
	Mounir Moutaouakil	2616			
The MAILING DATE of this communication Period for Reply	appears on the cover sheet with	the correspondence address			
A SHORTENED STATUTORY PERIOD FOR REI WHICHEVER IS LONGER, FROM THE MAILING Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory per Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the may be arrived patent term adjustment. See 37 CFR 1.704(b).	B DATE OF THIS COMMUNICA 1.136(a). In no event, however, may a repriod will apply and will expire SIX (6) MONTH atute, cause the application to become ABAI	ATION. ly be timely filed HS from the mailing date of this communication. NDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 30	<u> December 2003</u> .				
2a) ☐ This action is FINAL . 2b) ☑ T	☐ This action is FINAL . 2b) ☑ This action is non-final.				
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is				
closed in accordance with the practice unde	er <i>Ex parte Quayle</i> , 1935 C.D.	11, 453 O.G. 213.			
Disposition of Claims					
4)⊠ Claim(s) <u>1-20</u> is/are pending in the applicati	ion.				
4a) Of the above claim(s) is/are without	drawn from consideration.				
5) Claim(s) is/are allowed.	•				
6)⊠ Claim(s) <u>1-20</u> is/are rejected.					
7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and	d/or election requirement				
o) Claim(s) are subject to restriction are	u/or election requirement.				
Application Papers					
9)☐ The specification is objected to by the Exam	niner.				
10)⊠ The drawing(s) filed on <u>30 December 2003</u> i	, , , , , , , , , , , , , , , , , , , ,	•			
Applicant may not request that any objection to t					
Replacement drawing sheet(s) including the cord 11) The oath or declaration is objected to by the	· = ·				
The path of declaration is objected to by the	Examiner. Note the attached v	Office Action of form F10-132.			
Priority under 35 U.S.C. § 119					
12) ☐ Acknowledgment is made of a claim for forea) ☐ All b) ☐ Some * c) ☐ None of:	ign priority under 35 U.S.C. § 1	119(a)-(d) or (f).			
1. Certified copies of the priority docum					
2. Certified copies of the priority docum	•	<u> </u>			
 Copies of the certified copies of the p application from the International Bur 	•	eceived in this National Stage			
* See the attached detailed Office action for a	•	eceived			
det ind altaened detailed enter ter a	not or the continue copies het re				
Attachment(s)					
1) Notice of References Cited (PTO-892)	4) Interview Su				
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) 		/Mail Date ormal Patent Application			
Paper No(s)/Mail Date	6) Other:	···			

Art Unit: 2616

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the control plane in communication with the data plane must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filling date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1, 3, 6-9, 13-15, and 20 are rejected under 35 U.S.C. 102(e) as being anticipated by Clarke et al (US 2004/0213284). Hereinafter referred to as Clarke.

Regarding claim 1. Clarke discloses a network device (200, fig 2B) that comprises a data plane (204, 214) for transmitting data between an ingress port and an egress port; and a control plane (processor 208 and memory 254) in communication with the data plane, the control plane includes a shared transmit/receive queue infrastructure configured to queue incoming multicast packets to be replicated on a per ingress port basis and to queue transmit packets (see figure 2B, see page 2, column [0023] where the processors function in a round robin fashion to process queued incoming multicast packets), and a multicast processing engine in communication with the shared transmit/receive queue infrastructure (figure 2B, processor 208) the multicast processing engine including a circular replication buffer (see figure 2B. where the output buffer is a circular buffer) to facilitate multithreaded replication of multicast packets on a per egress virtual local area network (VLAN) replication basis (see figure 1. where multicast and frames are transmitted to and from VLAN networks).

Application/Control Number: 10/749,034

Art Unit: 2616

Regarding claim 3. Clarke discloses a network device, in which the shared transmit/receive queue infrastructure dynamically allocates memory to the transmit packets (see page 3, paragraph 0025, where the transmit packets are stored within the buffer memory) and to the incoming multicast packets to be replicated (see page 3, paragraph 0025, frames are replicated depending on the headers information).

Regarding claim 6. Clarke discloses a network device, in which the control plane further includes a packet parser configured to input queue a multicast packet header in the shared transmit/receive queue infrastructure on a per ingress port basis (See page 2, paragraph 0019, the input buffer includes a packet parser).

Regarding claim 7. Clarke discloses a network device, in which the packet parser is further configured to de-queue a multicast packet from the shared transmit/receive queue infrastructure, the de-queued multicast packet corresponding to an ingress port as determined by the multicast processing engine (see page 2, paragraph 0022. the packet parsing is mainly arranged to de-queue multicast packet form the input buffer. The processing engine determines the de-queuing).

Regarding claims 8, 13 and 20. Clarke discloses a network device, in which the multicast-processing engine forwards a replicated multicast packet onto a main control plane pipeline when traffic on the main control plane pipeline allows (see page 3, paragraph 0025, the replicated multicast packets are transmitted to the main pipeline whenever they are allowed depending on the flow).

Regarding claims 9 and 14. Clarke discloses a network device in which the control plane further includes a policer module configured to receive replicated multicast

Application/Control Number: 10/749,034

Art Unit: 2616

packet on the main control plane pipeline from the multicast processing engine (see paragraph 0024, where the system comprises a policer module), the main control plane pipeline containing at least one of unicast, layer 2 (L2), and multi-protocol label switching (MPLS) traffic (see paragraph 0016. where some of the data transmitted is MPLS traffic).

Regarding claim 15. Clarke discloses a computer program package embodied on a computer readable medium, the computer program package including instructions that, when executed by a processor (see page 1, paragraph 0012, the multicast processing system includes a computer readable medium), cause the processor to perform actions that comprises queuing incoming multicast packets to be replicated on a per ingress port basis in a shared transmit/receive queue infrastructure (figure 2B, the input buffer queues incoming multicast packets in order to be processed and replicated), the shared transmit/receive queue infrastructure being configured to queue the incoming multicast packets to be replicated and transmit packets (see figure 2B, the input buffer queues incoming multicast packets in order to be processed and replicated); determining an ingress port from which to de-queue multicast packets (see figure 2B, the input controller); de-queuing multicast packets from the shared transmit/receive queue infrastructure (see page 2, paragraph 0023, where the processor de-queues the packets from the input buffer), the de-queued multicast packets being associated with the determined ingress port and placed into a replication buffer for replication (figure 2B, output buffer); and performing multithreaded replication

Application/Control Number: 10/749,034 Page 6

Art Unit: 2616

of multicast packets on a per egress virtual local area network (VLAN) replication basis utilizing a replication buffer (figure 1, page 3 paragraphs 025-026).

Claim Rejections - 35 USC § 103

- 4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 5. The factual inquiries set forth in Graham v. John Deere Co., 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
 - 1. Determining the scope and contents of the prior art.
 - 2. Ascertaining the differences between the prior art and the claims at issue.
 - 3. Resolving the level of ordinary skill in the pertinent art.
 - Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Application/Control Number: 10/749,034

Art Unit: 2616

7. Claims 2, 4, 5, 10-12, and 16-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Clarke in view of Calamvokis et al (US 6,856,622). Hereinafter, referred to as Calamvokis.

Regarding claims 2, 11, 16 and 17. Clarke discloses all the limitations of claim 1.

Clarke does not disclose a network device, in which the multicast processing engine is configured to request multicast packets from the shared transmit/receive queue infrastructure upon emptying a slot in the circular replication buffer the requested multicast packet being from an ingress port determined based on a bandwidth management policy implemented by the multicast processing engine, and in which the multicast processing engine empties a slot in the circular replication buffer when all replications for the multicast packet occupying the slot are performed. However, Calamvokis discloses a cell scheduling protocol method in which the scheduler requests multicast packets from the ingress port as soon as the output circular buffer generate an empty slot (see figure1, elements 102A, 102B, and 106). Thus, it would have been obvious to a person of ordinary skill in the art at the time of the invention to implement the method of multicast cell scheduling protocol, as taught by Calamvokis, into the multicast processing unit of Clarke for the purpose of facilitating the scheduling of the multicast packets and maintaining a high-quality data flow.

Regarding claims 4 and 18. Clarke discloses all the limitations of claim 1.

Clarke does not disclose a network device, in which the multicast processing engine includes a scheduler utilizing scheduling algorithms to dynamically adapt the rate at which multicast packets are de-queued for each ingress port as a function of how

Art Unit: 2616

much output bandwidth each ingress port utilizes. However. Calamvokis discloses a cell scheduling protocol, in which the scheduler coordinates the de-queuing rates between the ingress and egress ports. Thus, it would have been obvious to a person of ordinary skill in the art at the time of the invention to implement the method of multicast cell scheduling protocol, as taught by Calamvokis, into the multicast processing unit of Clarke for the purpose of facilitating the scheduling of the multicast packets and maintaining a high-quality data flow.

Regarding claims 5, 12 and 19. Clarke discloses a network device, in which plurality of threads of replication in the circular replication buffer are maintained (see page 3, paragraph 025-026).

Clarke does not disclose a scheduler that is configured to request multicast packets from the shared transmit/receive queue infrastructure. However, Calamvokis discloses a cell scheduling protocol, in which the scheduler coordinates the de-queuing rates between the ingress and egress ports. Thus, it would have been obvious to a person of ordinary skill in the art at the time of the invention to implement the method of multicast cell scheduling protocol, as taught by Calamvokis, into the multicast processing unit of Clarke for the purpose of facilitating the scheduling of the multicast packets and maintaining a high-quality data flow.

Regarding claim 10. Clarke discloses a control plane multicast packet processing engine, that comprises a circular replication buffer for facilitating multithreaded replication (figure 2B, where the output buffer is a circular replication buffer for multicast replication) multicast packets on a per egress virtual local area network (VLAN)

Art Unit: 2616

replication basis (figure 1. where multicast and frames are transmitted to and from VLAN networks).

Clarke does not disclose a network device, in which a scheduler is in communication with a shared transmit/receive queue infrastructure for queuing incoming multicast packets to be replicated on a per ingress port basis and for queuing transmit packets, the schedule being configured to de-queue multicast packets associated with the ingress ports into the circular replication buffer, the scheduler utilizing scheduling algorithms to dynamically adapt the rate at which the multicast packets are de-queued from each ingress port as a function of how much output bandwidth each ingress port utilizes (figure 1). However. Calamvokis discloses a cell scheduling protocol, in which the scheduler coordinates the de-queuing rates between the ingress and egress ports. Thus, it would have been obvious to a person of ordinary skill in the art at the time of the invention to implement the method of multicast cell scheduling protocol, as taught by Calamvokis, into the multicast processing unit of Clarke for the purpose of facilitating the scheduling of the multicast packets and maintaining a high-quality data flow.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO-892.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mounir Moutaouakil whose telephone number is 571Application/Control Number: 10/749,034 Page 10

Art Unit: 2616

270-1416. The examiner can normally be reached on Monday-Thursday (4pm-4: 30pm) eastern time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou can be reached on 571-272-3088. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Mounir Moutaouakil Art Unit: 2616

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600